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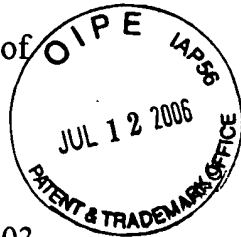
In re Patent Application of

DOTTA et al.

Serial No. 10/670,244

Filed: September 26, 2003

For: SEMICONDUCTOR DEVICE AND CHIP-
STACK SEMICONDUCTOR DEVICE



Confirmation No. 8593

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Examiner: Andujar, L.

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July 12, 2006

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
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RE-SUBMISSION OF REPLACEMENT DRAWINGS

Sir:

Enclosed herewith are three sheet(s) of replacement drawings containing Figs. 11-13, which were labeled "prior art" as requested by the Examiner as previously filed with the Amendment dated July 13, 2005 for the above-identified application.

Respectfully submitted,

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By: _____

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